

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Bachelor of Technology (Computer and Communication Engineering)2016-2017
Choice Based Credit System (CBCS)

SEMESTER - VII

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL		Th	T	P		
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)					
1	BTCS601	Compiler Design	60	20	20	30	20	3	1	2	5	150
2	BTCC703	Next Generation Telecommunication Network	60	20	20	30	20	3	1	2	5	150
3	BBAI501	Human Values and Professional Ethics	60	20	20	0	0	4	0	0	4	100
4	BTIT604	Object Oriented Analysis And Design	60	20	20	30	20	3	1	2	5	150
5		Elective-III	60	20	20	30	20	3	1	2	5	150
6	BTCS705	Industrial Training	0	0	0	0	100	0	0	4	2	100
7	BTCS706	Project	0	0	0	120	80	0	0	8	4	200
			300	100	100	240	260	16	4	20	30	1000
	Elective-III											
	BTCS701	Cloud Computing										
	BTIT611	Information Storage And Management										
	BTIT703	Design Patterns										
	BTCS612	Simulation and Modeling										

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


 Chairperson
 Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


 Joint Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore